



K25U 0931

Reg. No. :

Name :

**IV Semester B.C.A. Degree (C.B.C.S.S. – OBE-Regular/Supplementary/
Improvement) Examination, April 2025
(2019 to 2023 Admissions)
Core Course
4B09BCA : COMPUTER ORGANIZATION**

Time : 3 Hours

Max. Marks : 40

**PART – A
(Short Answer)**

Answer **all** questions.

(6×1=6)

1. What do you mean by micro-operations ?
2. List various computer registers.
3. What do you mean by DMA ?
4. Explain the term associative memory.
5. What do you mean by instruction-level parallelism ?
6. What is the difference between point-to-point and multipoint interconnection structures ?

**PART – B
(Short Essay)**

Answer **any 6** questions.

(6×2=12)

7. What do you mean by instruction cycle ?
8. Compare RISC and CISC architectures.
9. Elaborate the concept of serial communication.
10. Short note on I/O interfaces.
11. Short note on various modes of data transfer.

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12. Compare SRAM and DRAM.
13. Mention few characteristics of cache memory.
14. What do you mean by associative memory ?

PART – C
(Essay)

Answer **any 4** questions.

(4×3=12)

15. Write short notes on instruction codes.
16. What are the different types of instruction formats ?
17. Explain the concept of synchronous data transfer.
18. Write short note on cache memory.
19. Write notes on multiprocessors.
20. Explain the concepts of SIMD and MIMD architecture.

PART – D
(Long Essay)

Answer **any 2** questions.

(2×5=10)

21. Describe the different types of addressing modes in a processor.
 22. Describe various types of parallel processing and their advantages.
 23. Explain the concepts of serial communication.
 24. Briefly explain about virtual memory.
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