M 7520

Reg. No. : .....

Name : .....

III Semester B.C.A. Degree (CCSS – Reg./Supple./Imp.) Examination, November 2014
(BCA – Core Course)
3B07 BCA : COMPUTER ORGANIZATION

Time: 3 Hours Max. Marks: 21

SECTION – A

Answer **all** questions. Weightage for a bunch of **four** questions is **1** :

1. Which of the following is a method of data transfer ?
   a) Hand shaking b) Strobing
   c) Both d) None of the above

2. _____ is positioned logically between CPU registers and main memory.

3. To make faster processing of information the speed of _____ must be faster.
   a) RAM b) ROM
   c) System clock d) None of these

4. The time required to process data and instructions for a microcomputer is calculated in _____

5. When two numbers of n' digits each are added and the sum occupies n + 1 digits we say that an _____ occurs.

6. The octal equivalent of the binary real number 1011.1011 is _____

7. A register that holds the instruction or data to be fetched from memory is called _____

8. In a _____ two addresses one specified for the two operands and one address for the result. **(2×1=2)**

P.T.O.

SECTION – B

Answer **any 5** questions. Weightage **1 each** :

9. What is auxiliary memory ?
10. What is a priority interrupt ?
11. What is bus transfer ?
12. Explain reverse polish notation.
13. What are peripheral devices ?
14. What is asynchronous data transfer ?
15. What is a control word ?
16. What is an accumulator ? **(5×1=5)**

SECTION – C

Answer **any five** questions. Weightage **2 each** :

17. Explain the fetch cycle.
18. Compare direct and indirect addressing mode.
19. Compare synchronous and asynchronous data transfer.
20. Explain the various cache schemes.
21. What is associative mapping ?
22. Distinguish between MAR and MBR.
23. Explain memory transfer.
24. Explain floating point representation of numbers. **(5×2=10)**

SECTION – D

Answer **one** question. Weightage **4** :

25. Explain the various addressing modes.
26. Describe the hierarchy of memory. **(1×4=4)**
