K16U 0116

Reg. No. : .....

Name : .....

VI Semester B.C.A. Degree (CCSS-Reg./Supple./Improv.) Examination, May 2016
Core Course
6B20 BCA : INTRODUCTION TO MICROPROCESSORS

Time : 3 Hours Max. Weightage : 21

SECTION – A

Answer **all** questions. Weightage for a bunch of **four** questions is **1**.

1. The microprocessor that followed 8080 is
   a) 8086 b) 8088 c) 8085 d) 8008

2. The 8086 microprocessor has _____ segment register.
   a) 8 b) 16 c) 4 d) 0

3. The CPU 8086 is able to address _____ of physical memory.
   a) 1 MB b) 2 MB c) 3 MB d) 4 MB

4. MOV AX, [S1] represents _____ addressing mode.
   a) direct b) immediate c) indexed d) register

5. Division by zero is an example of
   a) internal interrupt b) external interrupt
   c) trap d) none

6. The maskable interrupt pin is
   a) NMI b) $\overline{INTA}$ c) INTR d) CLK

7. The fastest data transfer scheme
   a) DMA b) Programmed I/O
   c) Interrupt driven d) none

8. The program that is initiated when interrupts occur is
   a) INTR b) $\overline{INTA}$ c) ISR d) NMI

(2×1=2)

P.T.O.

SECTION – B

Answer **any 5** questions. Weightage **1 each**.

9. Give the general data registers used in 8086.
10. What is the use of a flag register ?
11. Give features of 8085.
12. What is the use of INTA signal ?
13. What are internal interrupts ?
14. What is an interrupt service routine ?
15. What is DMA ?
16. List few arithmetic instructions of 8086. **(5×1=5)**

SECTION – C

Answer **any 5** questions. Weightage **2 each**.

17. Explain the bus organization of 8085.
18. Describe the register organisation of 8086.
19. Discuss about the instruction set in 8086.
20. Describe stack structure of 8086.
21. Explain the interrupt cycle in 8086.
22. Explain about DMA controller 8257.
23. Compare maskable and now maskable interrupts. **(5×2=10)**

SECTION – D

Answer **any one** question. Weightage **4**.

24 Explain the architecture of 8086.

25. Discuss about addressing modes in 8086. **(1×4=4)**
